

ABSTRACT OF THE DISCLOSURE

This invention is intended to notify a user that a clock count means included in a video receiver has been corrected. The video receiver 15 includes a microcomputer 10. An antenna 1, a tuner 2, and a monitor output unit 20 are connected to the video receiver 15. The microcomputer 10 of the video receiver 15 includes the clock count section 6, a time data extraction processing section 5, and a clock correction processing section 7. A broadcast signal input through the antenna 1 and the tuner 2 is input to the clock data extraction processing section 5 of the microcomputer 10, and the clock data extraction processing section 5 acquires correction time information. The clock correction processing section 7 compares data on time counted by the clock count section 6 with the correction time information, and thereby corrects the clock count section 6. By outputting the time data obtained by the clock count section 6 and the correction time information both of which are input to the clock correction processing section 7 to the monitor output unit 20, the user is notified that the clock count section 6 has been corrected.